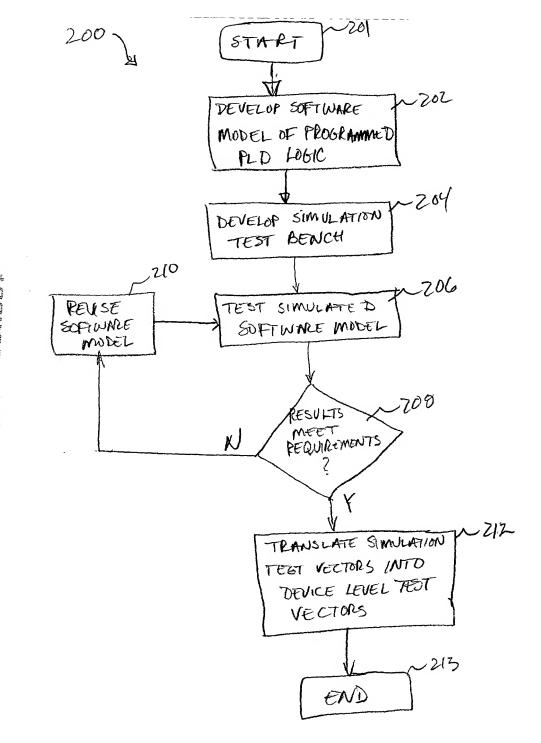
DESIGN 1200
[IMPLEMENTATION]

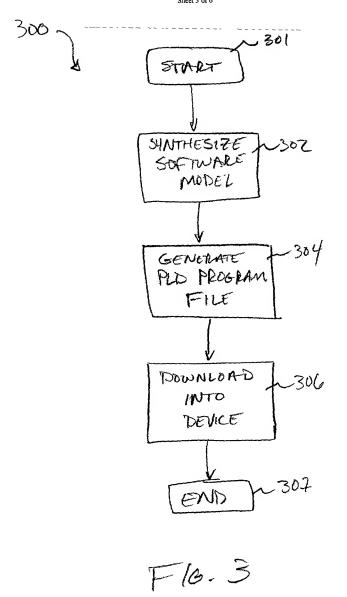
VERLETCHTON

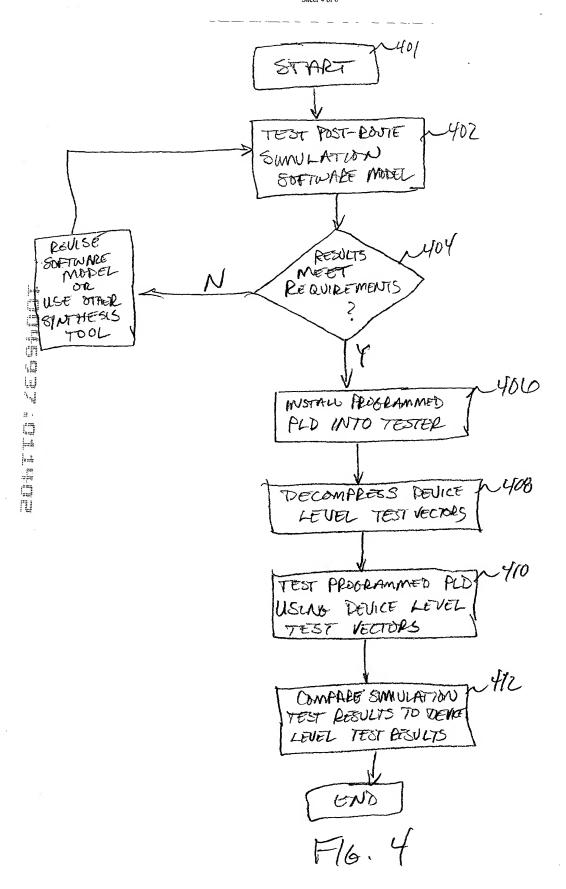
Active Land and the first land the first land that the first land

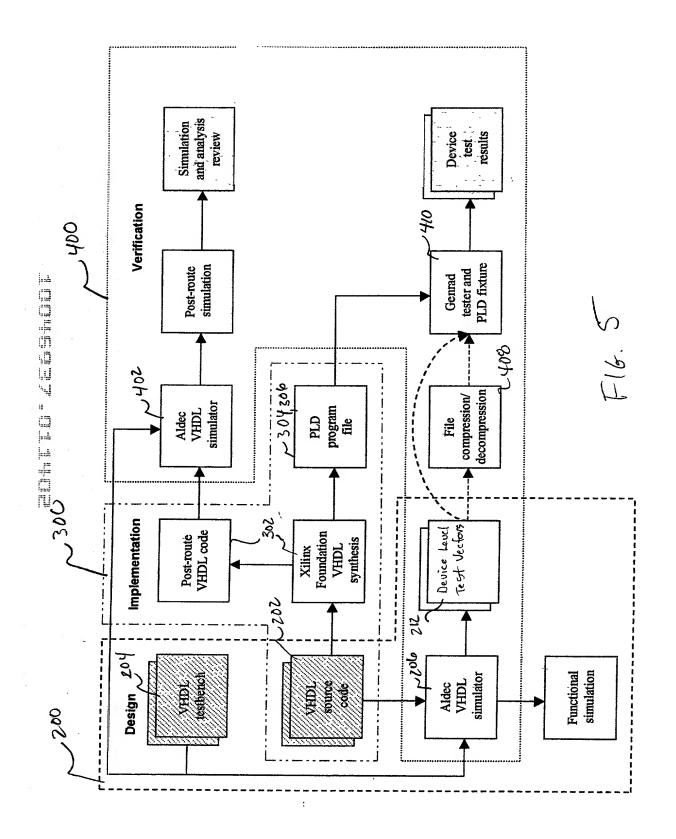


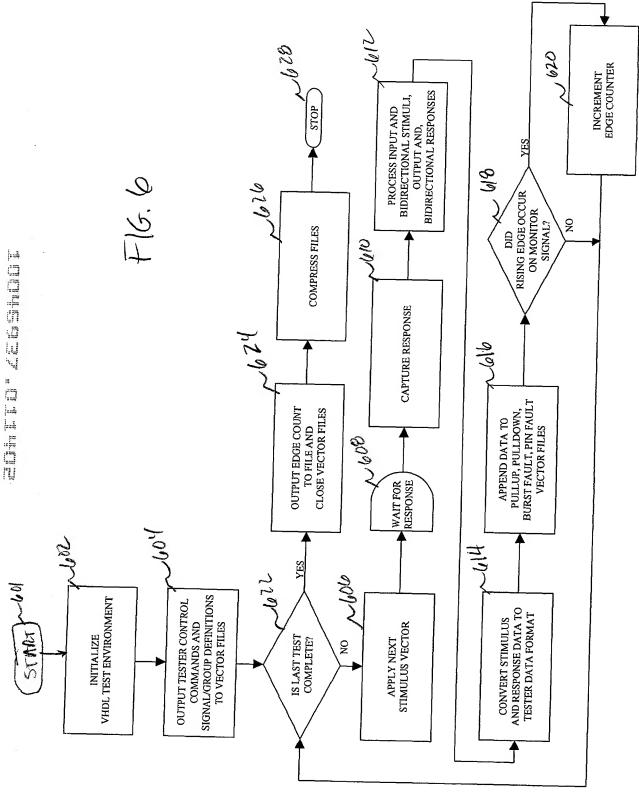
F16.2

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